

a plurality of channel regions extending in a column direction respectively in said semiconductor region;

a plurality of picture elements in which electric charges are accumulated defined in each portion of said plurality of channel regions; and

a plurality of transfer electrodes, extending in a row direction on said semiconductor region, for transferring the electric charges accumulated in said plurality of channel regions, said plurality of transfer electrodes being allocated to each picture element,

*see below*  
said plurality of picture elements including:

light receiving elements in which the electric charges are accumulated in accordance with the incident light, and storage elements in which the electric charges transferred from said light receiving elements are stored,

said plurality of light receiving elements including:

a first set of a plurality of light receiving elements in which at least one of the corresponding transfer electrodes is activated and simultaneously at least one of the transfer electrodes is inactivated in first and second image pickup operations; and

a second set of a plurality of light receiving elements in which all of the corresponding transfer electrodes are inactivated in the first image pickup operation, and at least one of the transfer electrodes is activated and simultaneously at least one of the transfer electrodes is inactivated in the second image pickup operation.

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